



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein

Title: INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE
UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD
THEREFOR

Application No.: 09/990,894

Filed: November 16, 2001

Examiner: Yoha, Connie C.

Group Art Unit: 2818

NOA Date: N/A

Issue Batch: N/A

Atty. Docket No.: 023-0013

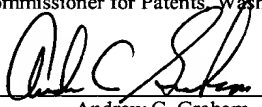
April 30, 2003

OFFICIAL DRAFTSPERSON
COMMISSIONER FOR PATENTS
Washington, DC 20231

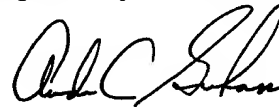
SUBMISSION OF NEW FORMAL DRAWINGS

Transmitted herewith are 9 sheets of new drawings containing Fig. 1; Fig. 2; Fig. 3; Fig. 4; Fig. 5; Fig. 6; Figs. 7 and 8; Fig. 9; and Fig. 10, respectively.

These formal drawings are being submitted prior to the first Office Action on the merits to facilitate the Examiner's review, and in hopes that the Official Draftsperson will review these formal drawings rather than the informal drawings submitted with the application as filed.

CERTIFICATE OF MAILING	
I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231,	
 Andrew C. Graham	<u>4-30-2003</u> Date

Respectfully submitted,



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